

Inverted Can Detector 1




Operating Description

The state of pulse input 1 P1, and pulse input 2 P2, are continually monitored. If both inputs are high at the same time then a inverted can has been detected.

When a inverted can has been detected the value 100 is saved in memory 4 M4 causing relay 1 latch on.

Relay 1 will remain on until the reset button is pressed causing more than 5 volts input to CH1 which then saves 0 in M4 causing relay 1 to release.

USC Programs

File	Comment
 Help	If unexpected results occur when loading the .usc file press back and click on help for instructions.
 UAP00191.usc	First issue
 UAP00192.usc	Update to 105 standard.

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