

# Ramp Output Connection - BSI134

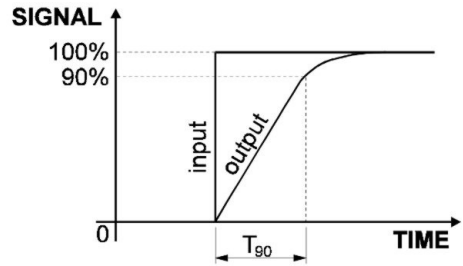
This drawing applies to output option 01 on the BSI134

## Timing Capacitor Selection

$T^{90}$  is defined as: for an input step of 0 - 100%, the output ramps up to 90% in  $T^{90}$  seconds

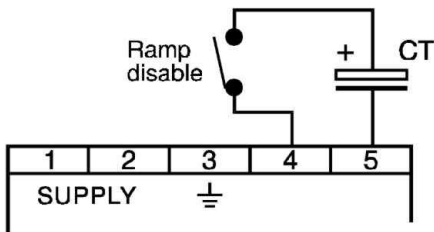
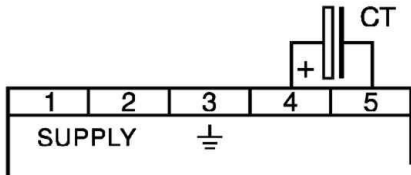
e.g. 18.4mA for 4 - 20mA input signal).

CT	T90
2 $\mu$ F	1 sec
22 $\mu$ F	5 sec
47 $\mu$ F	10 sec
100 $\mu$ F	20 sec
147 $\mu$ F	30 sec



## Connection

CT can be an electrolytic capacitor or tag, minimum voltage 16V.



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