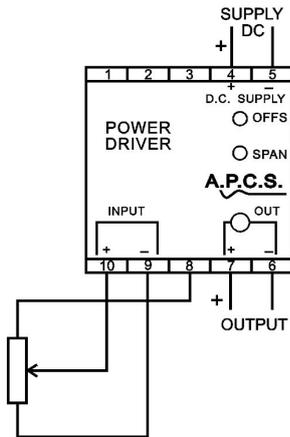


Optional inputs fro PD121

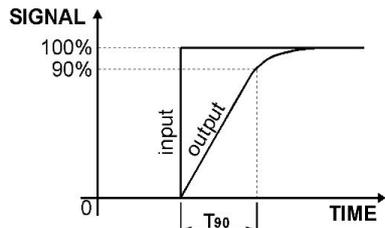
Input 25 - 3-Wire Pot

A reference voltage is supplied in terminal 8 of the PD121. The potentiometer or slide wire is connected between the reference (pin 8) and 0V (pin 9). The wiper output is then used as a voltage input to perform the re-transmit function.



Output reference voltage: 5V, 3mA
 Minimum potentiometer resistance: 1k

Option 01 - Output Ramp



T90 is defined as: for an input step of 0 - 100%, the output ramps up to 90% in T90 seconds, i.e. 18.4mA for 4 - 20mA input.

TIMING CAPACITOR SELECTION

CT	T90
2μF	1 sec
22μF	5 sec
47μF	10 sec
100μF	20 sec
147μF	30 sec

CT can be an electrolytic capacitor or tag, minimum voltage 16V.

Option 15 - External Gain And Bias

The range of the gain and bias adjustments are manufactured to the customer requirements. Single turn 5k potentiometers are included

If a gain range specified is 0 - 100% and the nominal input is 0-10V for 0-10mA output then;

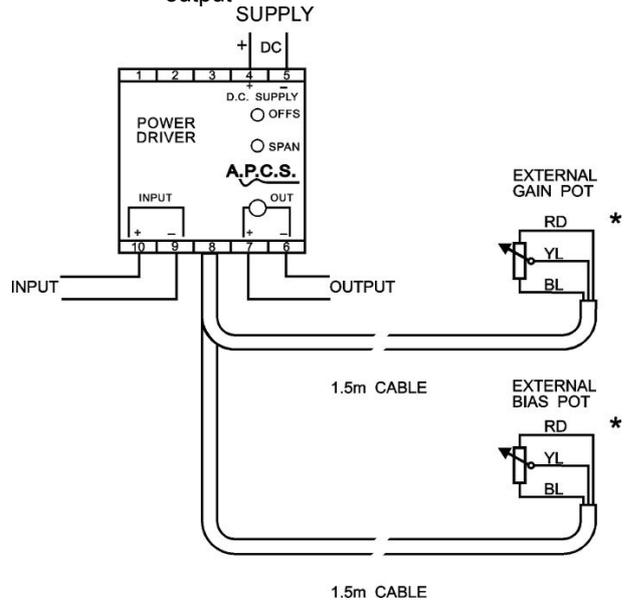
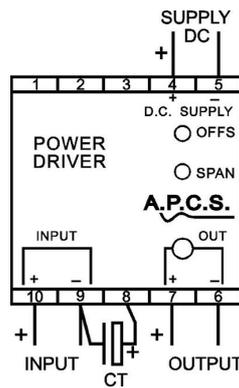
- When the gain is set to the minimum the output range for 0-10V in will be 0-0mA. out.
- When the gain is set to the maximum the output range for 0-10V in will be 0-10mA out.

If a gain range specified is 50 - 150% and the nominal input is 0-10V for 0-10mA output then;

- When the gain is set to the minimum the output range for 0-10V in will be 0-5mA. out.
- When the gain is set to the maximum the output range for 0-6.67V in will be 0-10mA out.

The range of the bias adjustment can be 0 - 100%.

- If the gain is set at 100%, and bias is set to 10% then 1-10V input results in a 0-9mA output.
- If the gain is set at 100%, and bias is set at 50% then 5-10V input results in 0-5mA output



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